UNIVERSITATEA TEHNIÇÂ

UNIVERSITATEA TEHNICĂ DIN CLUJ-NAPOCA



SYLLABUS

1. Data about the program of study

1.1 Institution	Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Electronics, Telecommunications and Information
1.2 Faculty	Technology
1.3 Department	Bases of Electronics
1.4 Field of study	Electronic Engineering, Telecommunications and Information
1.4 Field of Study	Technologies
1.5 Cycle of study	Bachelor of Science
1.6 Program of study / Qualification	Telecommunications Technologies and Systems/ Engineer
1.0 Program of Study / Qualification	Applied Electronics/Engineer
1.7 Form of education	Full time
1.8 Subject code	TST-E25.00/EA-E25.00

2. Data about the subject

2.4.6. 1.1		A I	Analysis and Synthesis of Circuits					
2.1 Subject name		Anaiys	sis a	na Sy	Inthesis of Circuits			
2.2 Subject area		Theor	Theoretical area					
2.3 Course responsib	Course responsible/lecturer Assist. Prof. Ioana SARACUT, Ph.D <u>Ioana.Saracut@bel.utcluj.r</u>				cluj.ro			
2.4 Teachers in charge with seminary / laboratory Assist. Prof. Ioana SARACUT, Ph.D Ioana.Saracut@bel. Assist. Prof. Ervin SZOPOS, Ph.D Erwin.Szopos@bel.utclu Assist. Prof. Calin FARCAS, Ph.D Calin.Farcas@bel.utclu				Erwin.Szopos@bel.utclu	j.ro			
2.5 Year of Study	П	2.6 Semeste	er	4	2.7 Assessment	Ε	2.8 Subject category	DD/DI

3. Estimated total time

4	of which: 3.2 course	4	3.3 applications	2	
56	of which: 3.5 course	28	3.6 applications	28	
Distribution of time					
ibliog	raphy			28	
Supplementary study in the library, online specialized platforms and in the field					
Preparation for seminars/laboratory works, homework, reports, portfolios, essays					
Tutoring					
Exams and tests					
	56 ibliog	of which: 3.5 course sibliography nline specialized platforms and	of which: 3.5 course 28 ibliography nline specialized platforms and in	56 of which: 3.5 course 28 3.6 applications sibliography nline specialized platforms and in the field	

3.7 Total hours of individual study	69
3.8 Total hours per semester	125
3.9 Number of credit points	5

4. Pre-requisites (where appropriate)

4.1 Curriculum	Knowledge acquired in Signals and Systems course.
4.2 Competence	Relations and theorems for electric circuits.



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5. Requirements (where appropriate)

5.1 for the course	Amphitheatre, Cluj-Napoca
5.2 for the seminaries / laboratory classes	Laboratory, Cluj-Napoca

6. Specific competences

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Professional competences	C1. Use of the fundamental elements related to devices, circuits, systems, instrumentation and electronic technology C2. Applying the basic methods for the acquisition and processing of signals C4. Design, implementation and operation of data, voice, video and multimedia services. This is based on the understanding and the application of fundamental concepts in telecommunications and transmission of information
Transversal	N/A

7. Discipline objectives (as results from the key competences gained)

7.1 General objective	The development of the skills regarding the analysis and synthesis of passive and active systems.
7.2 Specific objectives	 Knowledge and understanding of basic approaches regarding analysis and synthesis of systems. Development of skills and abilities for the analysis and synthesis of passive circuits.

8. Contents

6. Contents	To a china Mathada	Damanla		
8.1 Lecture	Teaching Methods	Remarks		
Circuit analysis with signal flowgraphs.				
2. Stability analysis with linear invariant systems.	ion			
3. Graphical stability analysis criteria (Michailov,	tat			
Nyquist).	sen			
4. State space. Definitions of state variables.	pre:			
5. Formulation of state equations for a passive circuit.	problem presentation, : evaluation.	_		
6. Passive two-ports analysis. Symmetric and	roblem evaluati	ard		
nonsymmetrical two-ports.	pro ev	poś		
7. Applications of two-ports.		ack		
8. Matching of circuits.	exemplifications, le study, formative	Jse of the blackboard		
9. T, PI and Γ -shaped impedance matching circuits.	fica	the		
Rejection of frequencies with impedance matching	ipliid Jy,	of		
circuits.	xempli: study,	Jse		
10. Passive filters. Constant-k filters.		_		
11. Derived filters. Characteristic impedance correction.	ion			
12. Applications of filters.	tat			
13. System function approximation. Active filters:	Presentation, cas			
biquads	Pre			
14. Review. Examination preparation.	_			



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Bibliography			
The web page of the course: http://www.bel.utcluj.ro/scs	<u> </u>		
8.2 Seminary classes	Teaching Methods	Remarks	
1. Signal flowgraph.			
2. Stability criteria.	e .S		
3. State space.	of some didactic		
4. Passive two-ports.	of so		
5. Impedance matching circuits.		ن ق	
6. Constant-k and derived filters.	evie scts pro vorl	tboard	
7. Filters	d re spe tal m v	ckb t bo	
Laboratory classes	Solving of problems and review of some theoretical aspects. Didactic and experimental proof, didactic exercise, team work	Jse of the blackboard Use of Digilent board.	
1. Second order low, high and pass-band filters.			
2. Elementary one-ports.		of tl	
3. Simple T-form impedance matching circuits.	pro the id e exe	Jse o	
4. Impedance matching circuit with frequency	gof		
rejection.	/ing		
5. Constant-k filters.	Solving		
6. Active filters.]		
7. Lab classes recovery.			

Bibliography

Weekly homework problems submitted by email.

The web page of the course: http://www.bel.utcluj.ro/scs/

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field.

The discipline content and the acquired skills are in agreement with the expectations of the professional Competences acquired will be used in the following COR occupations (Electronics Engineer; Telecommunications Engineer; Electronics Design Engineer; System and Computer Design Engineer; Communications Design Engineer) or in the new occupations proposed to be included in COR (Sale Support Engineer; Multimedia Applications Developer; Network Engineer; Communications Systems Test Engineer; Project Manager; Traffic Engineer; Communications Systems Consultant).

10. Evaluations

Activity type	10.1 Evaluation criteria	10.2 Evaluation methods	10.3 Weight in the final grade			
10.4 Lecture	The level of acquired theoretical knowledge	2 written tests (30p) – TC	Max 30%			
10.5 Laboratory	The level of acquired skills and abilities	Evaluation during the semester (10p) – TL	Max 10%			
Exam	The level of acquired theoretical knowledge, of skills and abilities	Written examination (60p) – E	Max 60%			
Final mark = (TC+TL+E) / 10						
10.6 Minimum stand	ard of performance					
	TC+TL > 20p and E >	25p				



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Facultatea de Electronică, Telecomunicații și Tehnologia Informației

Date of filling in:	Responsible	Title First name SURNAME	Signature
	Course	Assist. Prof. Ioana SARACUT, Ph.D.	
	Applications	Assist. Prof. Ioana SARACUT, Ph.D.	
		Assist. Prof. Ervin SZOPOS, Ph.D.	
		Assist. Prof. Calin FARCAS, Ph.D.	

Date of approval in the Council of the Communications

Department
11.07.2023

Date of approval in the Council of the Faculty of Electronics,
Telecommunications and Information Technology
12.07.2023

Head of Communications Department
Prof. Virgil DOBROTA, Ph.D.

Dean
Prof. Ovidiu POP, Ph.D.