

ORAR Master 2017 - 2018, Semestrul I
Circuite si Sisteme Integrate (CSI), Anul I

Ziua/Ora		Grupa	
		1	2
Luni	08-10		
	10-12		
	12-14		
	14-16		
	16-18	PCDVLSI L/P 307	
	18-20		
Marti	08-10		
	10-12		
	12-14		
	14-16		
	16-18	FA L/P 502	
	18-20	CAA L 307	
Miercuri	08-10		
	10-12		
	12-14		
	14-16		
	16-18	FILTRE ANALOGICE C P01 Prof. dr. ing. M Topa	
	18-20	INT. COMP. IN SIST. DEC. COMPL. C P01 Prof. dr. ing. G Oltean-COM.ITS AeA	
Joi	08-10		ICSDC L 329
	10-12	ICSDC L 329	
	12-14		
	14-16		
	16-18	PROIECTAREA CIRCUITELOR DIGITALE VLSI C D22 Prof. dr. ing. S Hintea	
	18-20	CIRCUITE ANALOGICE AV. C D22 Conf. dr. ing. Doris Csipkes-COM.2IE	
Vineri	08-10		
	10-12		
	12-14		
	14-16		
	16-18		
	18-20		
Sambata	08-10		PCDVLSI L/P 307* CAA L307 * FA L/ P 502*
	10-12		
	12-14		
	14-16		
	16-18		
	18-20		*In cazul semigrpei 2, aplicatiile din zilele de sambata se vor desfasura modular, conform structurii alaturate

Sapt.	Data	Per.	Disciplina
S1	07.10.2017	8-14	CDVLSI-4L+2P
S2	14.10.2017	8-11	CAA 3L
S3	21.10.2017	8-14	FA 4L+2P
S4	28.10.2017	8-14	SDVLSI 4P+2L
S5	04.11.2017	8-11	CAA 3L
S6	11.11.2017	8-14	FA 4P+2L
S7	18.11.2017	8-14	SDVLSI 4L+2P
S8	25.11.2017	8-11	CAA 3L
S9	02.12.2017	8-14	FA 2L+4P
S10	09.12.2017	8-14	SDVLSI 4L+2P
S11	16.12.2017	8-11	CAA 3L
S12	23.12.2017	8-14	FA 6L
S13	13.01.2018	8-12	SDVLSI 4P
S14	20.01.2018	8-10	CAA 2L
		10-14	FA 4P